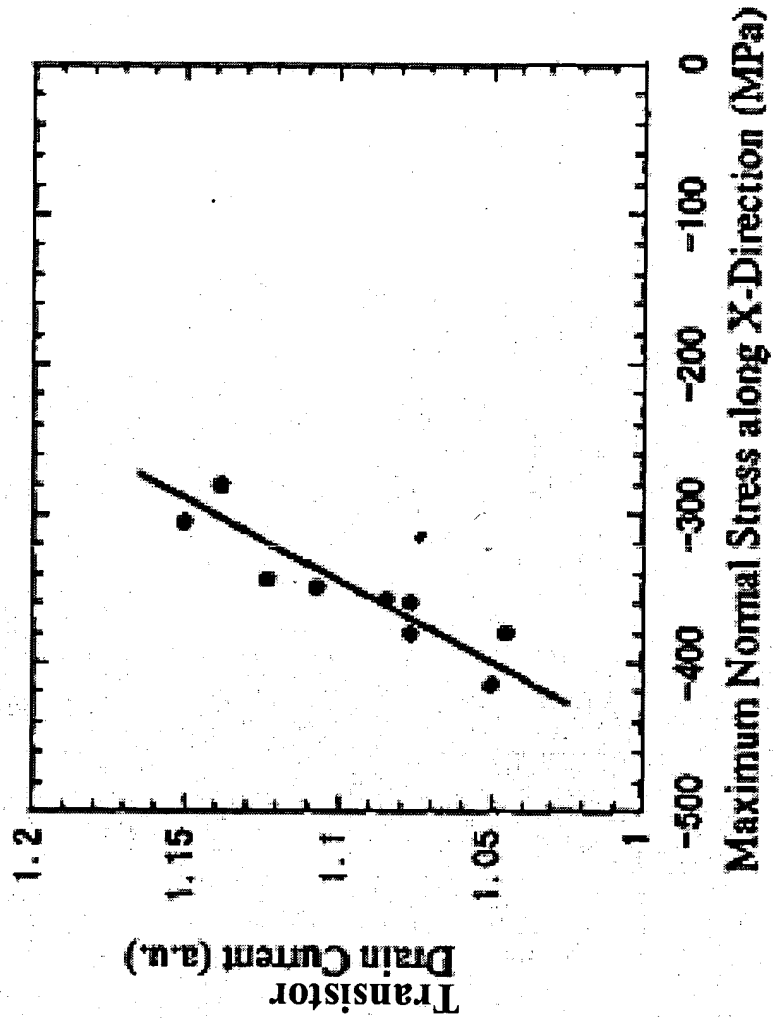


Fig. 1



**Fig. 2 NFET  $I_{ds}$  versus maximum mechanical stress under the gate.**

Fig. 2

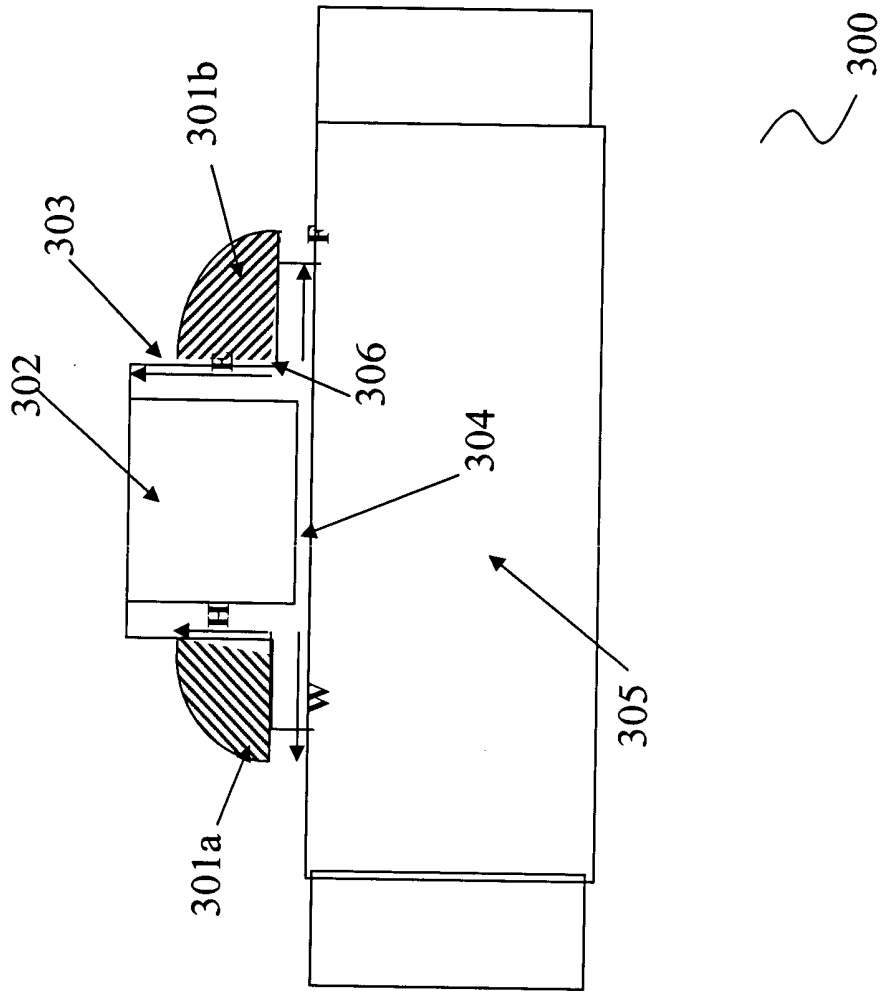


Fig. 3

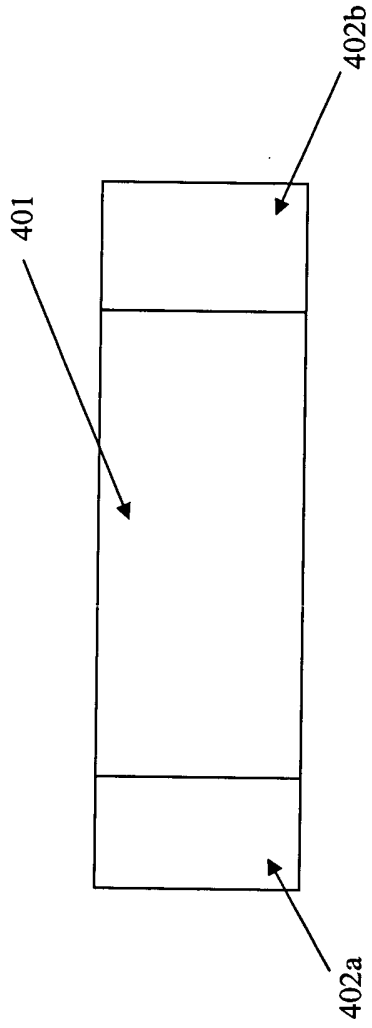


Fig. 4A

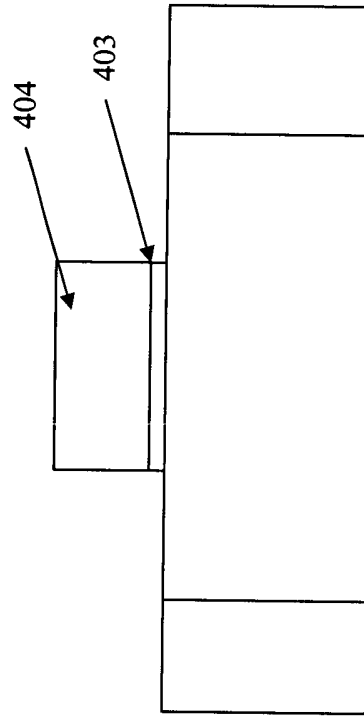


Fig. 4B

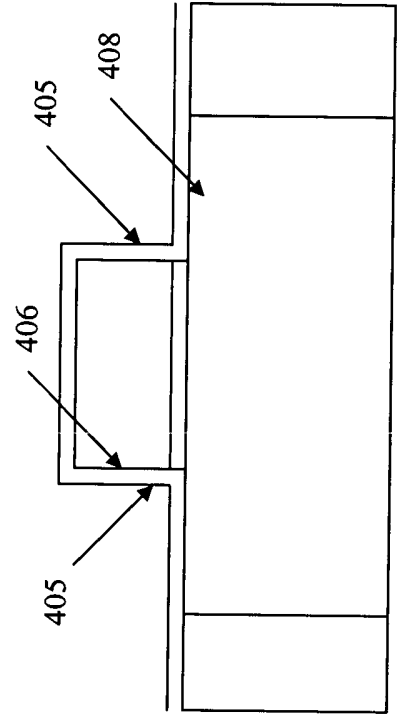


Fig. 4C

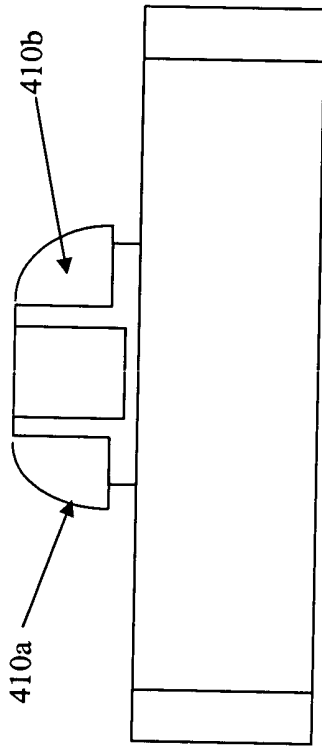


Fig. 4D

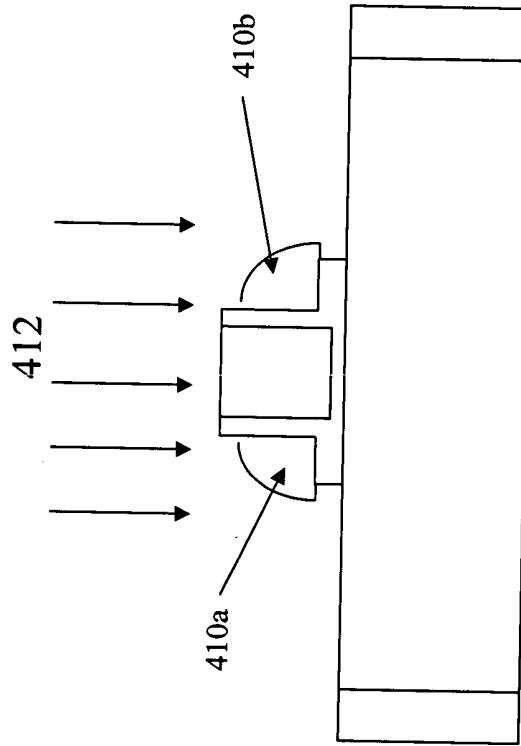


Fig. 4E